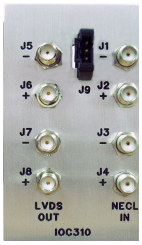
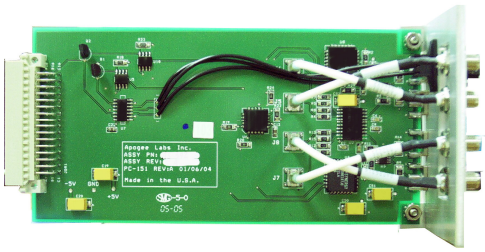
MODEL 2073 INTERFACER PRODUCT LINE

IOC312





DIFFERENTIAL NECL INPUT TO DIFFERENTIAL NECL OUTPUT BUFFER

FEATURES

- Differential NECL Input (-0.8V to -1.8V)
- Differential NECL Output (-0.8V to -1.8V)
- 1 Gbit operation
- Signal Input Activity indicator (LED)
- SMA Input/Output connectors

OVERVIEW

The IOC312 Pluggable Interface Module (PIM) accepts one Differential NECL input and converts it into two Differential NECL outputs. The IOC312 uses two SMA input connectors and two SMA output connector per differential signal. The IOC312 will operate to 1G bit. LED A will illuminate when there is an active differential NECL input on J1 and J2. The IOC312 requires two slots of the 14 available slots in the Model 2073 chassis.

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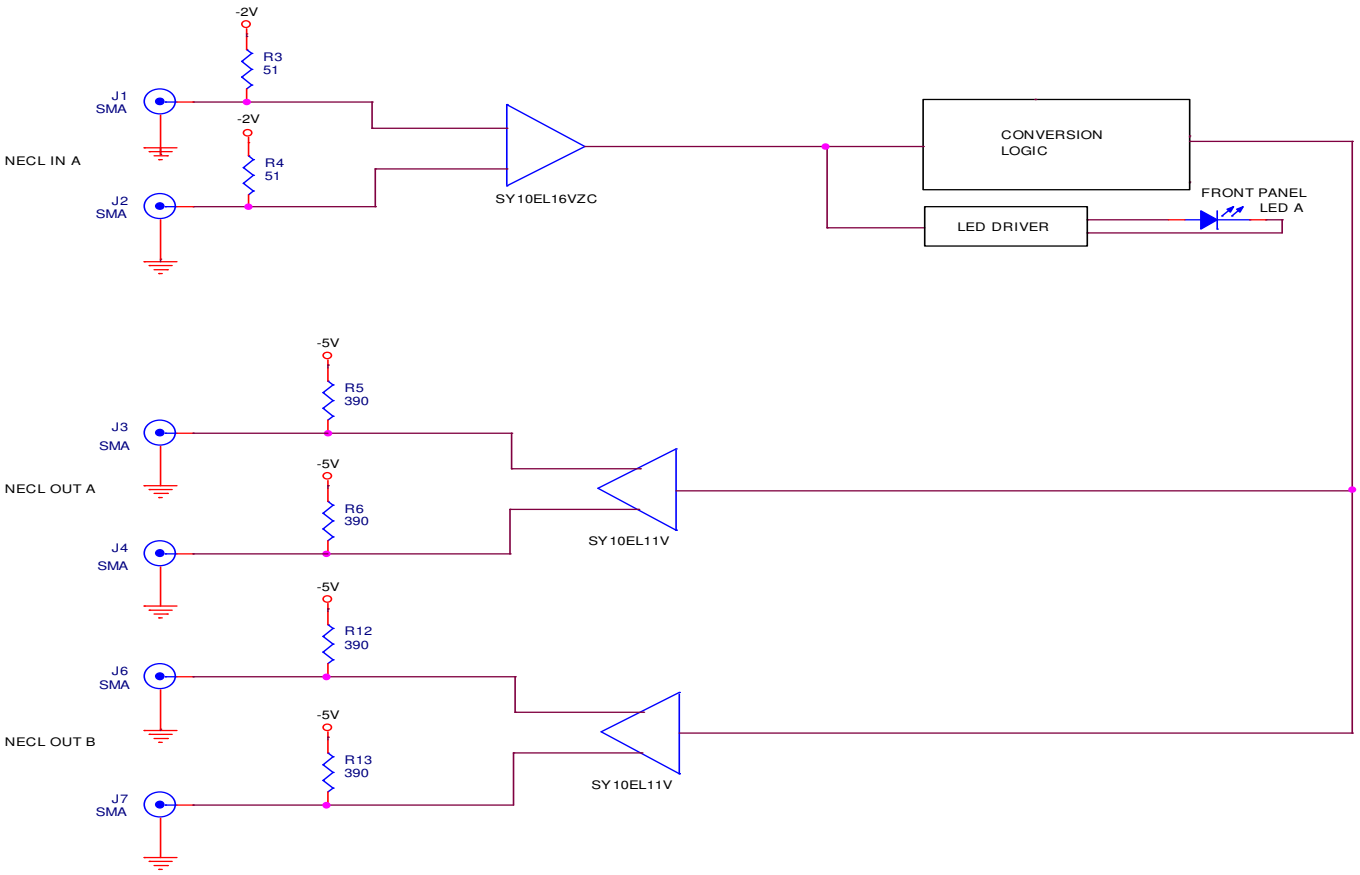

Figure 1: Model IOC312 Block Diagram

SPECIFICATIONS

GENERAL

Dual Slot Module (3" x 6" x 1.8")

Model 2073 Pluggable Interface Module

+5V, -5V

INPUT

1 each Differential NECL (-0.8V to -1.8V)

2 each SMA connectors per input

1G Bit Clock Rate

OUTPUT

2 each Differential NECL (-0.8 to -1.8V)

2 each SMA connectors per output

1G Bit Clock Rate

APPLICATION INFORMATION

The IOC312 is used to buffer differential NECL data and clock to differential NECL data and clock.

This joins equipment with unlike interfaces by properly receiving and driving the signals.

This module can be plugged into Apogee Models:

2097 and 2098: Data Acquisition Mux/Demux

6801: 5 Channel BERT Operation

6804: Multi Channel Clock Recovery Unit