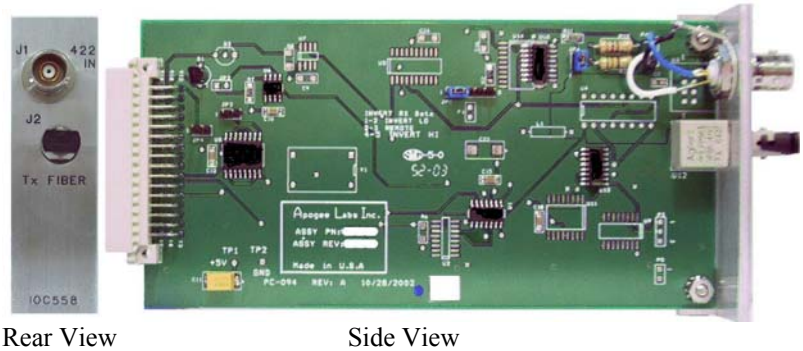


MODEL 2073 INTERFACER PRODUCT LINE

IOC558

RS-422 TO MULTI-MODE  
FIBER MODULE

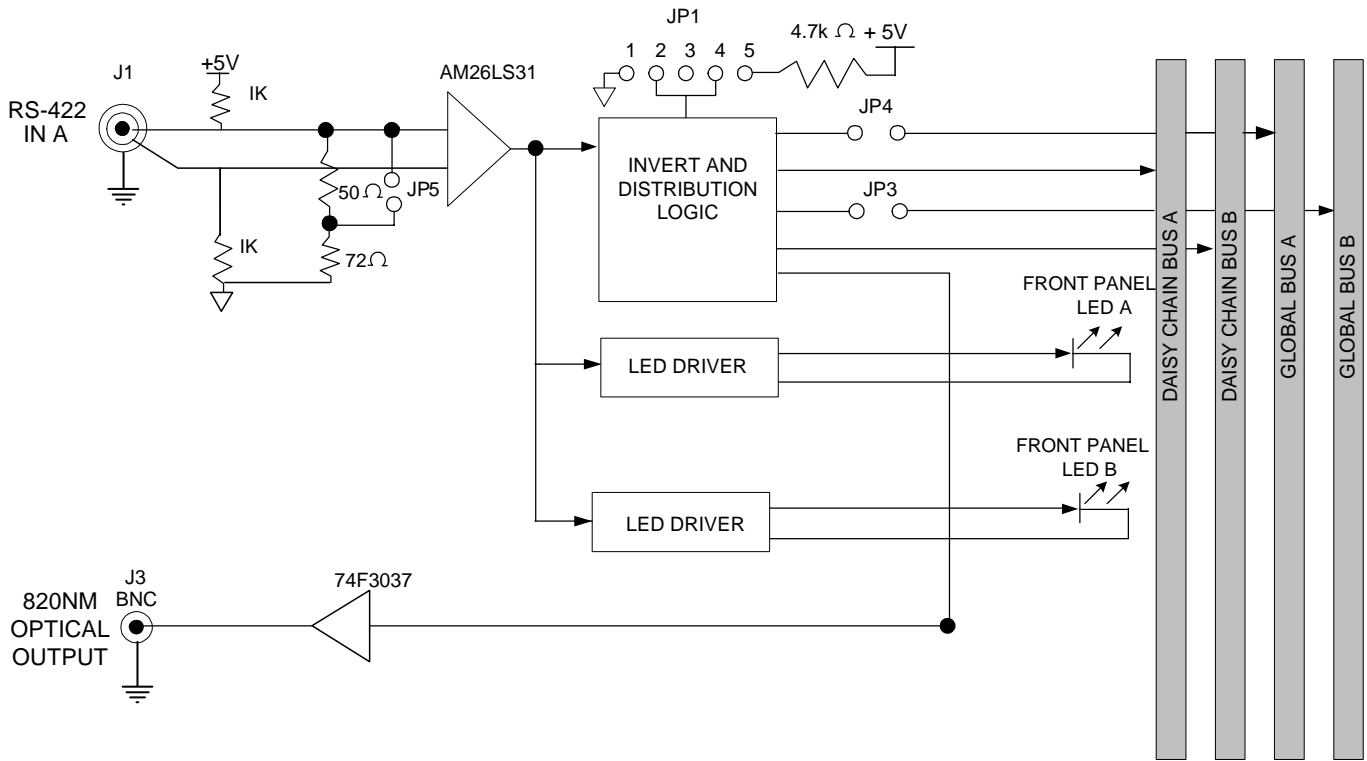


FEATURES

- RS-422 Input Level
- Input Drives the Daisy Chain and Global Buses
- Jumper selectable Input signal inversion
- Jumper selectable input termination 75/120 ohm
- 820nm Wavelength Optical Output
- Drives Multimode optical cable, 62.5/125um to 2.7 KM
- Operates from 1Mbps to 30 Mbps

OVERVIEW

The IOC558 PIM accepts an RS-422 level input from which it produces a multimode 820nm fiber optic output. The RS-422 input can be terminated into either 75 or 120 ohms and can be inverted. The RS-422 input can drive the 2073 chassis Daisy Chain and Global buses if additional copies or conversions of this signal are necessary. The optical output from the IOC558 meet the IEEE 802.3 Ethernet standards, providing a link distance up to 2.7 kilometers using 62.5/125um Fiber. The optical mating connector is ST. The IOC558 module is designed to operate from 1 Mbps to 30 Mbps data rate. The IOC558 requires one of the 14 available slots in the Model 2073 chassis.



**Figure 1: Model IOC558 Block Diagram**

**SPECIFICATIONS**

**GENERAL**

1 Conversion per card  
 Single Slot Module (3" x 6" x 0.9")  
 Model 2073 Pluggable Interface Module

**Input:**

J1 in RS-422 Triax Connector

**Output:**

J2 out Fiber Optic ST connector

**APPLICATION INFORMATION**

The IOC558 is used to convert/transmit an RS-422 level signal across a optical fiber.

The IOC558, when joined with the IOC557, is used to move RS-422 data at rates of up to 30 Mbps between long lengths of fiber optic cables.

This module can also be plugged into Apogee Models:

- 2907 and 2908: Data Acquisition Mux/Demux
- 6801: 5 Channel BERT Operation
- 6804: Multi Channel Clock Recovery Unit

Jumper	Function
JP1	RS-422 IN (J1), 1-2: Not Inverted, 4-5 Inverted
JP3	Short to drive Global Bus CK
JP4	Short to drive Global Bus DT
JP6	RS-422 IN Termination (J1) Open: 120Ω Short 75Ω

Apogee Labs Inc. products are sold by description only. Apogee Labs Inc. reserves the right to make changes in circuit design, software, hardware and/or specifications at any time without notice. Although Apogee Labs Inc. believes that the information provided is current and accurate, Apogee Labs Inc. does not assume any responsibility or liability for the use of any product described. It is the responsibility of the user to determine appropriate use of the product in any given application.