



APOGEE LABS

IOCDIGIN

DIGITAL MODULE

FEATURES

- 32 discreet bit / parallel data input module
- Up to 80 Ks/s
- TTL level high impedance inputs (1 K Ω)
- Selectable word location in PCM stream
- Selectable number of Words

OVERVIEW

The IOCDIGIN Pluggable Interface Module (PIM) is a discreet bit or parallel data input module that provides a mechanism to insert individual or parallel TTL data into an IRIG106 compliant PCM data stream. User setup features include the word location in the PCM stream and the number of words starting with the most significant bit or bit 31. The IOCDIGIN board provides status to the AL2873 LCD screen which shows the current bit status of all 32 bits regardless of how many are being transmitted. As part of the AL2873 Data Acquisition System, the IOCDIGIN module requires the IOCPCM2 which generates the IRIG106 compliant PCM stream. The IOCPCM2 reads data from the individual acquisition modules over the AL2873 PCM bus and inserts the digital information into the user defined PCM data stream. The IOCDIGIN uses one DB44 pin connector with input sampling up to 80,000 samples/sec. When data is included in the PCM stream the IOCDIGIN will illuminate the corresponding "A" LED on the 2873 chassis. The "B" LED on the 2873 chassis will illuminate when power is received to the board. The IOCDIGIN requires 1 slot of the 14 available slots in the Model 2873 chassis.

SPECIFICATIONS

GENERAL

- Single slot module (3" x 6" x 0.9")
- Model AL2873 pluggable interface module

INPUT

- 32 discreet bits
- DB44S socket type connector
- TTL high impedance
- Up to 80 Ks/s rate

REMOTE CONTROL

- APEX Compatible
- Telnet – Unix/Linux Telnet and Microsoft Telnet supported (Subset of functionality)

ENVIRONMENT

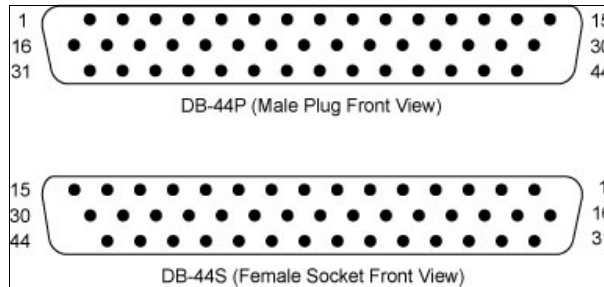
- Operating temperature: 0° C to +55° C
- Storage temperature: -40° C to +70° C

RELATIVE

- Humidity: up to 95% non-condensing

APPLICATIONS

The IOCDIGIN provides a mechanism to insert TTL level bit status or parallel data into a IRIG106 compliant PCM Data Stream. Typical data types are bit sync lock, frame sync lock, and receiver. Other signal types within module specifications are also supported.



DB44 connector pin locations (above) and pinout table (below)

J1 DISCREET BIT INPUT	
PIN	BIT
31	0, LSB
16	1
1	2
17	3
2	4
18	5
3	6
19	7
4	8
20	9
5	10
21	11
6	12
22	13
7	14
23	15
8	16
24	17
9	18
25	19
10	20
26	21
11	22
27	23
12	24
28	25
13	26
29	27
14	28
30	29
15	30
44	31,MSB
32-43	GND