

MODEL 2073 INTERFACER PRODUCT LINE

IOC014

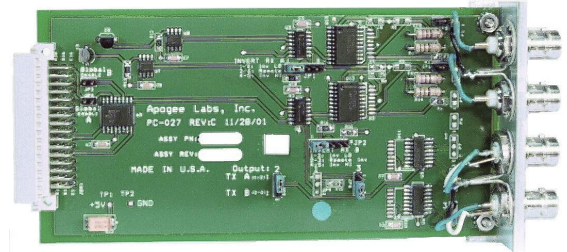
TTL INPUT SOURCE
SELECTOR

FEATURES

- 2 Pair TTL Level Inputs
- 1 Pair Selectable Output
- Internal or External Input Signal Switch Selector (Master/Slave)
- Inputs Illuminate Front Panel LEDs
- Selectable Input Termination (50 Ohm/75 Ohm)
- Selected Input Drives Daisy Chain Bus and Global Bus
- Independent Input Polarity Selection
- 5 Master/Slave Switch Modes



Rear View



Side View

OVERVIEW

The IOC014 Pluggable Interface Module (PIM) is a 1 of 2 pair input switching device. It accepts two sets of TTL Level input signals and switches 1 pair to the Daisy Chain and Global bus of the 2073 for eventual output by another 2073 module. Switching of the input signals can be accomplished using the front panel rotary switch or from another module in the chassis providing a master or slave configuration. Utilizing 5 bussed signals on the 2073 backplane, there can exist up to 5 master/slave configurations in a single 2073 chassis. All input signal polarities may be inverted independently. The IOC014 uses 4 BNC input connectors and operates up to 35 Mbps. It drives the Daisy Chain bus and the Global bus in the Model 2073 Chassis, enabling the user to create multiple copies of the output signals. The IOC014 requires one slot of the 14 available slots in the Model 2073 chassis.

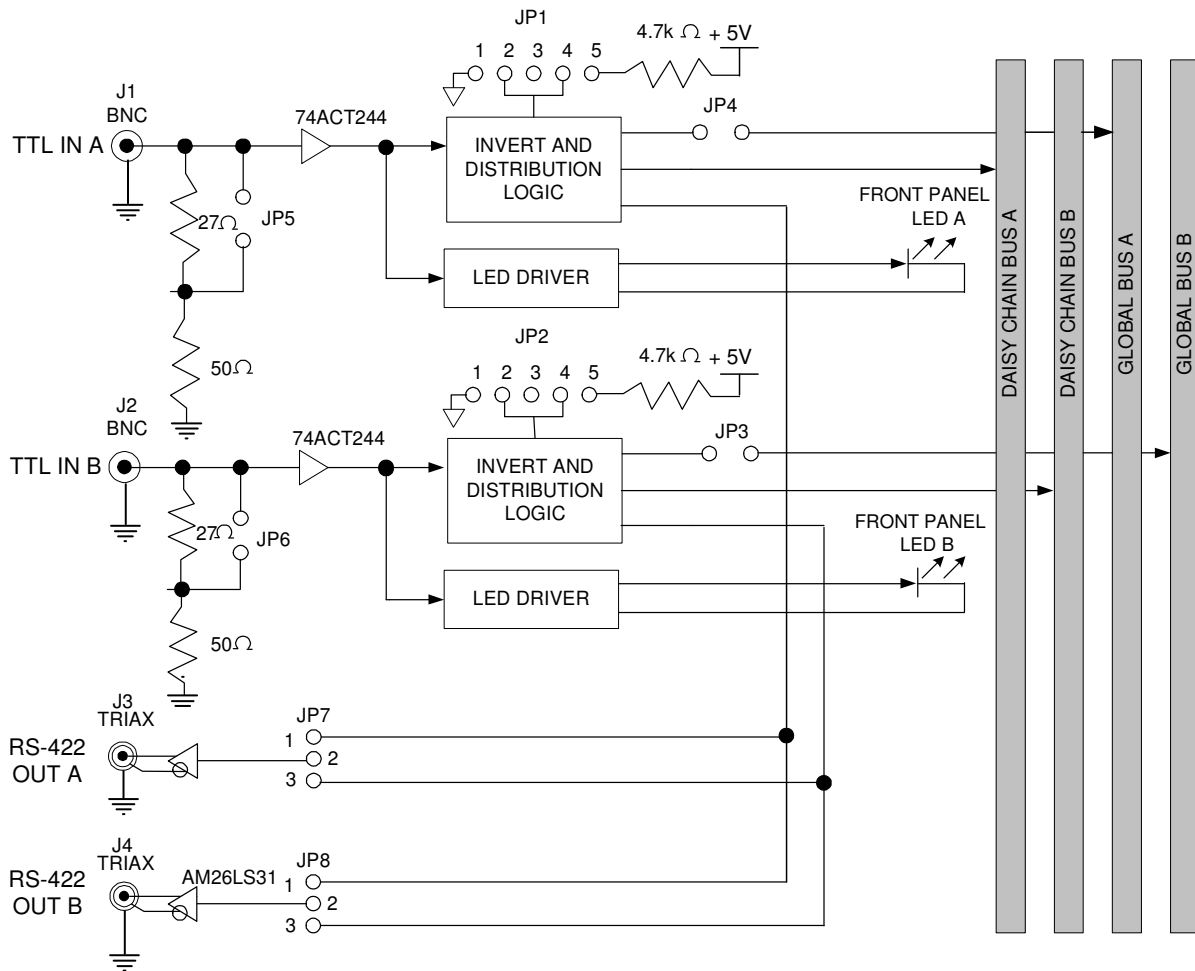


Figure 1: Model IOC014 Block Diagram

SPECIFICATIONS

GENERAL

- 2 Pair of Data/Clock Inputs
- 1 Pair of Switch Selected Data/Clock Outputs
- Single Slot Module (3" x 6" x 0.9")
- Model 2073 Pluggable Interface Module

INPUT

- TTL Level Inputs
- BNC Connectors
- 50/75-ohm selectable termination

OUTPUT

TTL on the 2073 Daisy Chain and/or Global Bus

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APPLICATION INFORMATION

The IOC014 is used is a switching configuration to select 1 of 2 possible inputs. A typical configuration is the selection of either realtime or playback data in a ground station enviroment. The IOC014 is used in a distribution application where the Daisy Chain bus or

Global bus is used to distribute the switch selected input signals.

JUMPERS	FUNCTION
JP1	Open pins 1-2, Short pins 4-5 to invert TTL IN A Short pins 1-2, Open pins 4-5 for non-inverted TTL IN A
JP2	Open pins 1-2, Short pins 4-5 to invert TTL IN B Short pins 1-2, Open pins 4-5 for non-inverted TTL IN B
JP3	Short pins 1-2 to drive Global Bus B, open to disable
JP4	Short pins 1-2 to drive Global Bus B, open to disable
JP5	Short pins 1-2 for 50 Ω TTL IN A input termination, Open for 75 Ω
JP6	Short pins 1-2 for 50 Ω TTL IN B input termination, Open for 75 Ω
JP7	Short pins 1-2 to send TTL IN A to RS-422 OUT A Short pins 2-3 to send TTL IN B to RS-422 OUT A
JP8	Short pins 1-2 to send TTL IN A to RS-422 OUT B Short pins 2-3 to send TTL IN B to RS-422 OUT B