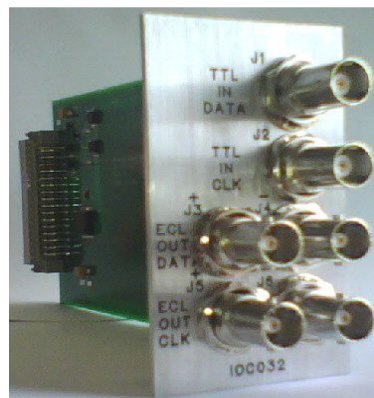


MODEL 2073 INTERFACER PRODUCT LINE

IOC032

TTL TO ECL CONVERTER MODULE



FEATURES

- Two Single Ended TTL level inputs
- Two Differential NECL level outputs (-0.8V to -1.8V)
- Inputs illuminate front panel LEDs
- Selectable input termination (50 ohm/75 ohm/10 Kohm)
- Drives Global Bus
- Independent output polarity selection

OVERVIEW

The IOC032 Pluggable Interface Module (PIM) accepts two TTL Level input signals and produces two differential NECL level output signals. Output polarities may be inverted independently. The IOC032 uses two BNC input connectors; four BNC output connectors and operates up to 35 Mbps. It can also drive the global bus in the Model 2073 Chassis, enabling the user to create multiple copies of the output signals. The IOC032 occupies two slots of the 14 available slots in the Model 2073 chassis.

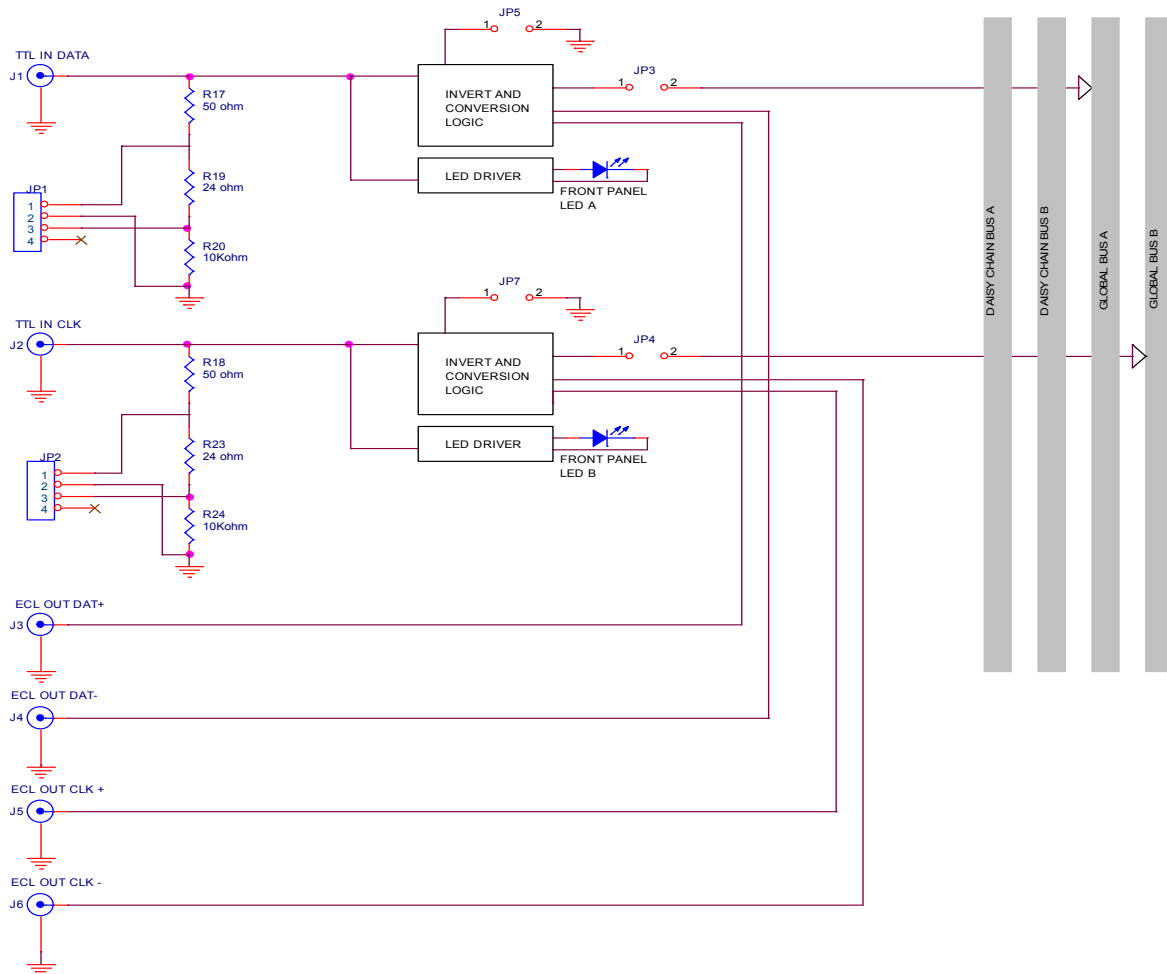


Figure 1: Model 2073-111-IOC032 Block Diagram

SPECIFICATIONS

GENERAL

4 Independent channels
 Dual Slot Model
 Model 2073 Pluggable Interface Module

INPUT

TTL Level Inputs
 BNC Connectors
 50/75/10k-ohm selectable termination

OUTPUT

ECL Level Outputs
 BNC Connectors

APPLICATION INFORMATION

The IOC032 can be used to convert any two TTL level signals to two ECL level signals. This helps join equipment with unlike interfaces by properly receiving and driving the signals.

The IOC032 can also be used in a distribution application where the global bus is used to distribute multiple copies of one or both input signals.

This module can also be plugged into Apogee Models:

2907 and 2908: Data Acquisition Mux/Demux

6801: 5 Channel BERT Operation

6804: Multi Channel Clock Recovery Unit

JUMPERS	FUNCTION
JP1	Short pins 1-2 for 50 Ω ; 2-3 for 75 Ω ; 3-4 for 10k Ω , TTL IN DATA
JP2	Short pins 1-2 for 50 Ω ; 2-3 for 75 Ω ; 3-4 for 10k Ω , TTL IN CLOCK
JP3	Short pins 1-2 to drive Global Bus A, open to disable
JP4	Short pins 1-2 to drive Global Bus B, open to disable
JP5	Short pins 1-2 for non-inverted TTL IN DATA, open to invert
JP7	Short pins 1-2 for non-inverted TTL IN CLOCK, open to invert